



501.42869X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: CHAKIHARA et al

Serial No.: 10/629,733

Filed: July 30, 2003

For: A Semiconductor Memory Device And A Method  
Of Manufacturing The Same, A Method of Manufacturing  
A Vertical MISFET And A Vertical MISFET, And A Method  
Of Manufacturing A Semiconductor Device And  
A Semiconductor Device

Art Unit:

Examiner:

**PRELIMINARY AMENDMENT**

Mail Stop: Amendment (No Fee)  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 20, 2003

Sir:

The following amendments are respectfully requested in connection with the  
above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification and Abstract

Remarks are included following the amendments